

What Is Claimed Is:

1. An active type display panel comprising a plurality of pixel sections each including a series circuit in which a light emitting element and a drive element which supplies a drive current to said light emitting element are connected in series, a pair of power supply lines which connect the series circuits of said plurality of pixel sections in parallel, and a plurality of measurement lines;

wherein each of said plurality of pixel sections includes a switch element which is provided between a point connecting said light emitting element and said drive element, and one measurement line of said plurality of measurement lines.

2. A display panel according to claim 1 further comprises a plurality of data lines, and a plurality of scan lines, and

each of said plurality of pixel sections comprises:

a first field effect transistor which is provided as said drive element and of which the source is connected to one line of the pair of power supply lines;

a second field effect transistor, of which the gate is connected to one scan line of said plurality of scan lines, the source is connected to one data line of said plurality of data lines, and the drain is connected to the gate of said first field effect transistor;

a capacitor connected between one line of the pair of power supply lines, and a line connecting the gate of said first field effect transistor and the drain of said second

field effect transistor;

an organic electroluminescence element which is provided as said light emitting element and of which the anode is connected to the drain of said first field effect transistor and the cathode is connected to the other line of the pair of power supply lines; and

a third field effect transistor which is provided as said switch element and of which the gate is connected to the one scan line, the source is connected to the one measurement line, and the drain is connected to a line connecting the drain of said first field effect transistor and the anode of said organic electroluminescence element.

3. A display device comprising:

an active type display panel comprising a plurality of data lines, a plurality of scan lines mutually intersecting said plurality of data lines, and a plurality of pixel sections each including a series circuit in which a light emitting element and a drive element which supplies a drive current to said light emitting element are connected in series, and which is connected between one of said plurality of data lines and one of said plurality of scan lines at an intersection thereof;

a power voltage supply portion which applies a power voltage to said series circuit of each of said pixel sections; and

a display controller which designates one scan line of said plurality of scan lines sequentially with predetermined

timing in accordance with an input image signal, supplies a scan pulse to the designated one scan line, and supplies a data signal indicating light emission luminance to at least one data line of said plurality of data lines in a scanning period during which the scan pulse is supplied, the at least one data line corresponding to at least one light emitting element to be emitted light on the designated one scan line,

wherein each of said pixel sections includes a pixel controller which activates said drive element in accordance with the data signal to supply a drive current of a level corresponding to the data signal to said light emitting element, and a voltage detector which detects a voltage across the terminals of said light emitting element; and

said display controller includes a data correction portion which corrects the data signal such that the voltage across the terminals of said light emitting element becomes equal to a predetermined voltage for each of said plurality of data lines.

4. A display device according to claim 3, wherein said display panel further comprises a plurality of measurement lines,

said drive element consists of a first field effect transistor of which the source is connected to the positive output terminal of said power voltage supply portion, and

said pixel controller comprises:

a second field effect transistor of which the gate is connected to the scan line for a corresponding column of said

plurality of scan lines, the source is connected to the data line for a corresponding row of said plurality of data lines, and the drain is connected to the gate of said first field effect transistor;

a first capacitor connected between the positive output terminal of said power voltage supply portion, and a line connecting the gate of said first field effect transistor and the drain of said second field effect transistor;

an organic electroluminescence element which is provided as said light emitting element and of which the anode is connected to the drain of said first field effect transistor and the cathode is connected to the negative output terminal of said power voltage supply portion; and

a third field effect transistor which is provided as said voltage detector and of which the gate is connected to the scan line for the corresponding column, the source is connected to the measurement line for the corresponding row of said plurality of measurement lines, and the drain is connected to a line connecting the drain of said first field effect transistor and the anode of said organic electroluminescence element,

wherein the voltage across the terminals of said light emitting element is supplied to said data correction portion as an anode voltage of said organic electroluminescence element through the drain and source of said third field effect transistor and the measurement line for the corresponding row.

5. A display device according to claim 4, wherein said data correction portion comprises:

a current generation circuit which generates a reference current of a level corresponding to the data signal;

a switching portion which halts activation of said drive element by said pixel controller by supplying the reference current in a first predetermined period appearing first in the scanning period during which the scan pulse is supplied, to said organic electroluminescence element through the measurement line for the corresponding row and through the source and drain of said third field effect transistor, and which permits activation of said drive element by said pixel controller by halting the supply of the reference current to said organic electroluminescence element in a second predetermined period which remains in the scanning period;

a holder which holds the anode voltage of said organic electroluminescence element in a second capacitor as the predetermined voltage, in the first predetermined period;

a comparing portion which outputs a correction voltage corresponding to the difference between the anode voltage of said organic electroluminescence element and the voltage held in said second capacitor, in the second predetermined period; and

a portion which supplies the correction voltage to said pixel controller through the data line for the corresponding row.

6. A display device according to claim 5, wherein said

switching portion supplies a voltage required for halting activation of said drive element by said pixel controller, to the source of said second field effect transistor.

7. A display device according to claim 6, where the voltage required for halting the activation of said drive element is a voltage equal to the power voltage.

8. A display device according to claim 4, wherein said data correction portion comprises:

- a voltage generation circuit which generates a voltage corresponding to the data signal as the predetermined voltage;

- a comparing portion which outputs a correction voltage corresponding to the difference between the anode voltage of said organic electroluminescence element and the output voltage of said voltage generation circuit; and

- a portion which supplies the correction voltage to said pixel controller through the data line for the corresponding row.

9. A driving method for an active type display panel comprising a plurality of data lines, a plurality of scan lines mutually intersecting said plurality of data lines, and a plurality of pixel sections each including a series circuit in which a light emitting element and a drive element for supplying a drive current to said light emitting element are connected in series, and which is connected between one of said plurality of data lines and one of said plurality of scan lines at an intersection thereof; comprising the steps

of:

applying a power voltage to said series circuit of each of said pixel sections;

designating one scan line of said plurality of scan lines sequentially with predetermined timing in accordance with an input image signal, supplying a scan pulse to the designated one scan line, and supplying a data signal indicating light emission luminance to at least one data line of said plurality of data lines in a scanning period during which the scan pulse is supplied, the at least one data line corresponding to at least one light emitting element to be emitted light on the designated one scan line;

in each of said pixel sections, activating said drive element in accordance with the data signal to supply a drive current of a level corresponding to the data signal to said light emitting element, and detecting a voltage across the terminals of said light emitting element; and

correcting the data signal such that the voltage across the terminals of said light emitting element becomes equal to a predetermined voltage for each of said plurality of data lines.